



ARINC653 AADL Annex Update

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AADL Meeting February 15



Report Documentation Page				Form Approved OMB No. 0704-0188	
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1. REPORT DATE 15 FEB 2015		2. REPORT TYPE N/A		3. DATES COVERED	
4. TITLE AND SUBTITLE ARINC653 AADL Annex Update				5a. CONTRACT NUMBER	
				5b. GRANT NUMBER	
				5c. PROGRAM ELEMENT NUMBER	
6. AUTHOR(S) Delange /Julien				5d. PROJECT NUMBER	
				5e. TASK NUMBER	
				5f. WORK UNIT NUMBER	
7. PERFORMING ORGANIZATION NAME(S) AND ADDRESS(ES) Software Engineering Institute Carnegie Mellon University Pittsburgh, PA 15213				8. PERFORMING ORGANIZATION REPORT NUMBER	
9. SPONSORING/MONITORING AGENCY NAME(S) AND ADDRESS(ES)				10. SPONSOR/MONITOR'S ACRONYM(S)	
				11. SPONSOR/MONITOR'S REPORT NUMBER(S)	
12. DISTRIBUTION/AVAILABILITY STATEMENT Approved for public release, distribution unlimited.					
13. SUPPLEMENTARY NOTES					
14. ABSTRACT					
15. SUBJECT TERMS					
16. SECURITY CLASSIFICATION OF:			17. LIMITATION OF ABSTRACT SAR	18. NUMBER OF PAGES 8	19a. NAME OF RESPONSIBLE PERSON
a. REPORT unclassified	b. ABSTRACT unclassified	c. THIS PAGE unclassified			

This material is based upon work funded and supported by the Department of Defense under Contract No. FA8721-05-C-0003 with Carnegie Mellon University for the operation of the Software Engineering Institute, a federally funded research and development center.

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DM-0002045



Context, Rationale

ARINC653

- Avionics standard
- Standardized API (called APEX – APplication EXecutive)
- Central part of the IMA philosophy
- Time & space partitioning

Rationale of ARINC653 annex for AADLv2

- Standardize modeling patterns
- Better modeling & analysis support
- Design associated toolset and framework



From the Last Meeting

Ballot with other annexes

Address most user comments

- Replace ARINC653 by ARINC 653

- Remove multi core examples

- Apply DAL on all software components

Get more user feedback

- Keep the document neutral, avoid subjective statements

Last editions ready to be published



Experiments with the update Annex

Latency Analysis

Analyze latency of inter-partitions communication

Code Generation for commercial ARINC653 OS

Demonstrate code generation for DeOS

Production of kernel- and partition-level code

Use of RESOLUTE to check ARINC653/AADL models

Check Model Compliance

Integration of an ARINC653-dedicated RESOLUTE library



Conclusion

Standardized modeling patterns for ARINC653 systems

- Support in OSATE and Ocarina

- Third-party support for implementation production

Generic annex that can be reused

- ARINC653 and MILS architectures

- Tailoring for other partitioned architectures

Exercised and demonstrated

- Architecture consistency checks and validation (i.e. latency)

- Code Generation of partitions and kernel



Links & Useful Information

AADL website – <http://www.aadl.info>

AADL wiki - https://wiki.sei.cmu.edu/aadl/index.php/Main_Page

ARINC653 AADL annex standard - <http://standards.sae.org/as5506/2/>



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